

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention	Capacity Expansion of Flash Memory Device with a Daisy-Chainable Structure and an Integrated Hub
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Application Number :

Confirmation Number:

First Named Applicant: Edward Lee

Attorney Docket Number: ML-15

Art Unit:

Examiner:

Search string: (6561421 or 6567273 or 6439464 or 6385677 or 6533612 or 6165016 or 6027375
or 5941733 or 5725395 or 6561421 or 20030100203 or 20030094490 or
20030145141 or 20030104835 or 20030093606).pn

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
3	1	6561421	2003-05-13	Yu	B1	235	451
2	2	6567273	2003-05-20	Liu et al.	B1	361	737
3	3	6439464	2002-08-27	Fruhauf et al.	B1	235	492
4	4	6385677	2002-05-07	Yao	B1	711	115
5	5	6533612	2003-03-18	Lee et al.	B1	439	607
6	6	6165016	2000-12-26	Lai	B1	439	610
7	7	6027375	2000-02-22	Wu et al.	B1	439	607
8	8	5941733	1999-08-24	Lai et al.	B1	439	610
9	9	5725395	1998-03-10	Lee et al.	B1	439	610
10	10	6561421	2003-05-13	Yu	B1	235	451

US Published Applications

Note: Applicant is not required to submit a paper copy of cited US Published Applications

init	Cite.No.	Pub. No.	Date	Applicant	Kind	Class	Subclass
1	1	20030100203	2003-05-29	Yen	A1	439	79
2	2	20030094490	2003-05-22	Lee	A1	235	441
3	3	20030145141	2003-07-31	Chen et al.	A1	710	74
4	4	20030104835	2003-06-05	Douhet	A1	455	557
5	5	20030093606	2003-05-15	Mambakkam et	A1	710	305

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Signature

Ryan Stiglic

Examiner Name	Date
Ryan Stiglic	9/13/05